

FIG. 1

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AMBAI31.01

MBUSI31.01

SBUSI31.01

22F

SEQ#_event_response

FBI_event_response

SRAM_event_response

SDRAM_event_response

amba_event_response

Context Event
Arbiter

72c

uPC_1

uPC_2

uPC_3

uPC_4

decode

immediate data

address

control store
1024 words

32bit words

FIG. 2-1

(A)

(B)

(C)

(D)

(E)

(F)

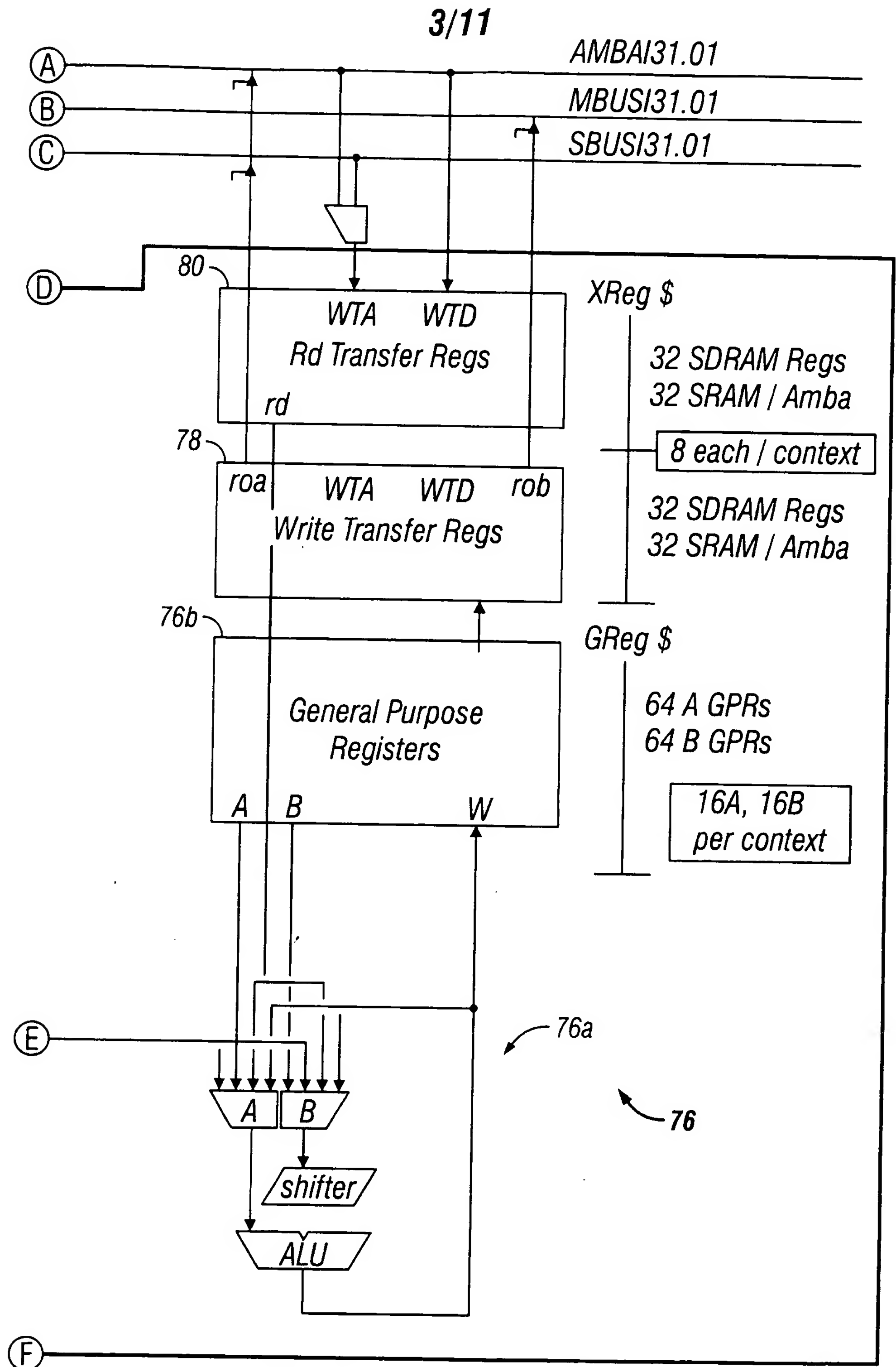


FIG. 2-2

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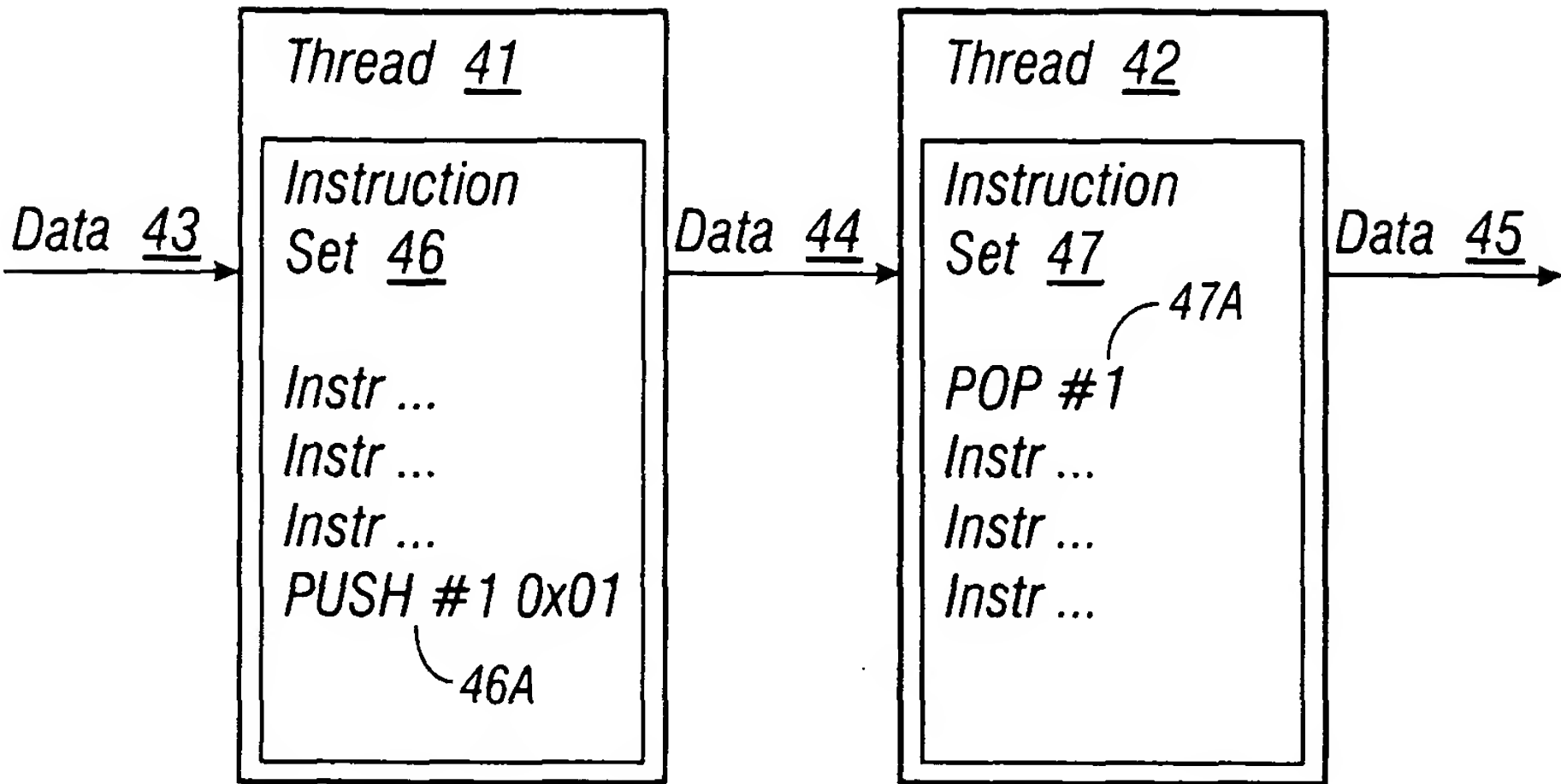


FIG. 3

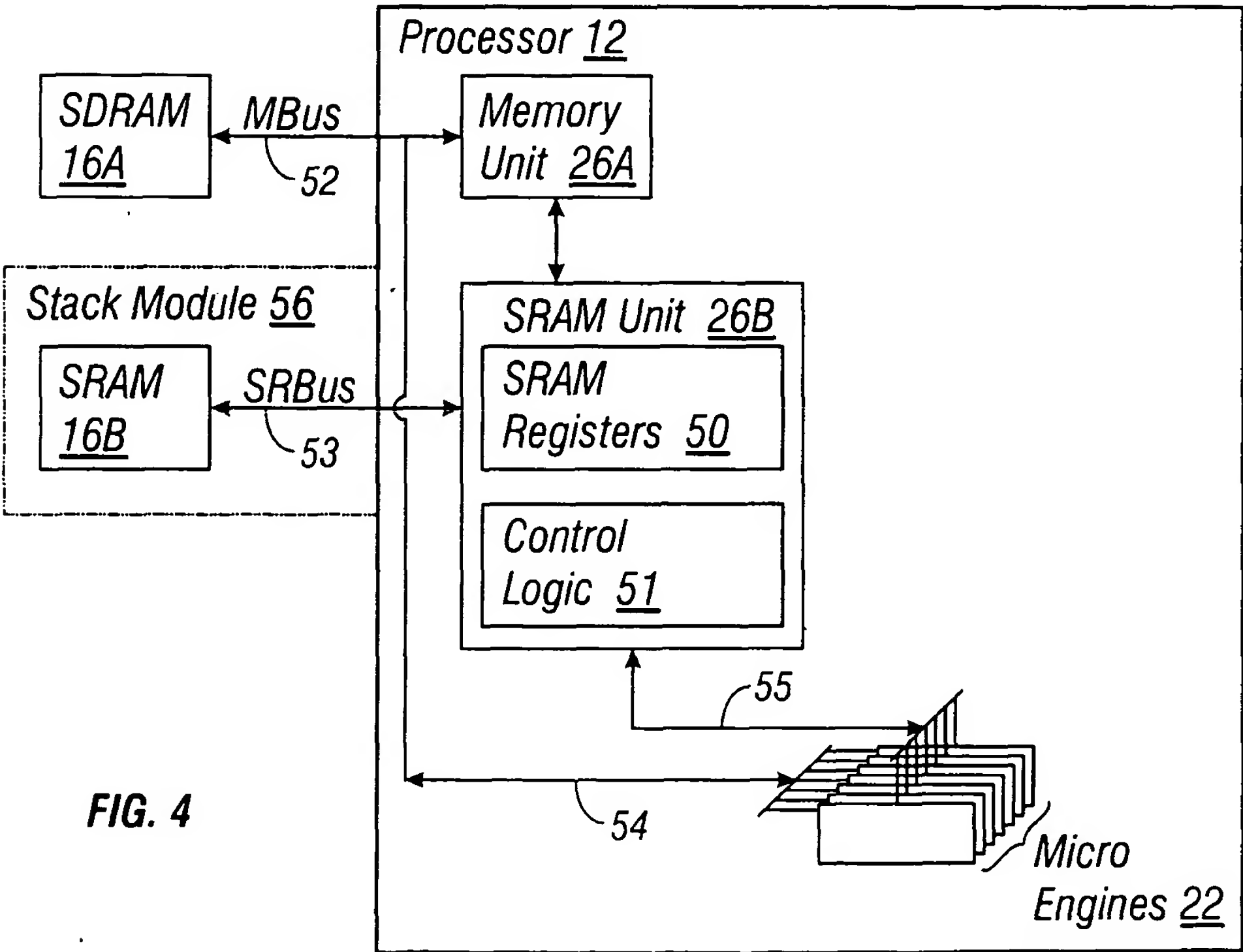


FIG. 4

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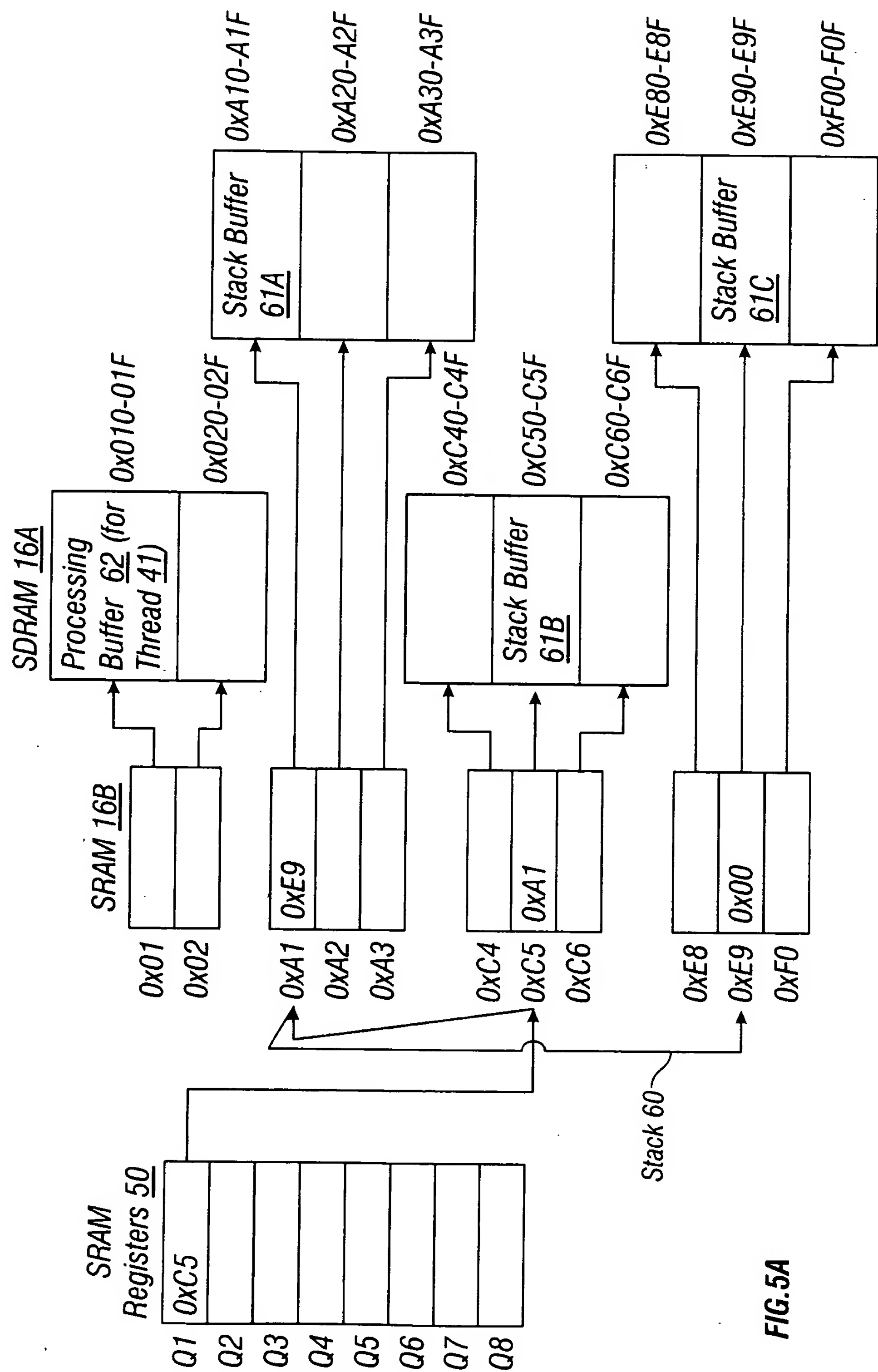


FIG.5A

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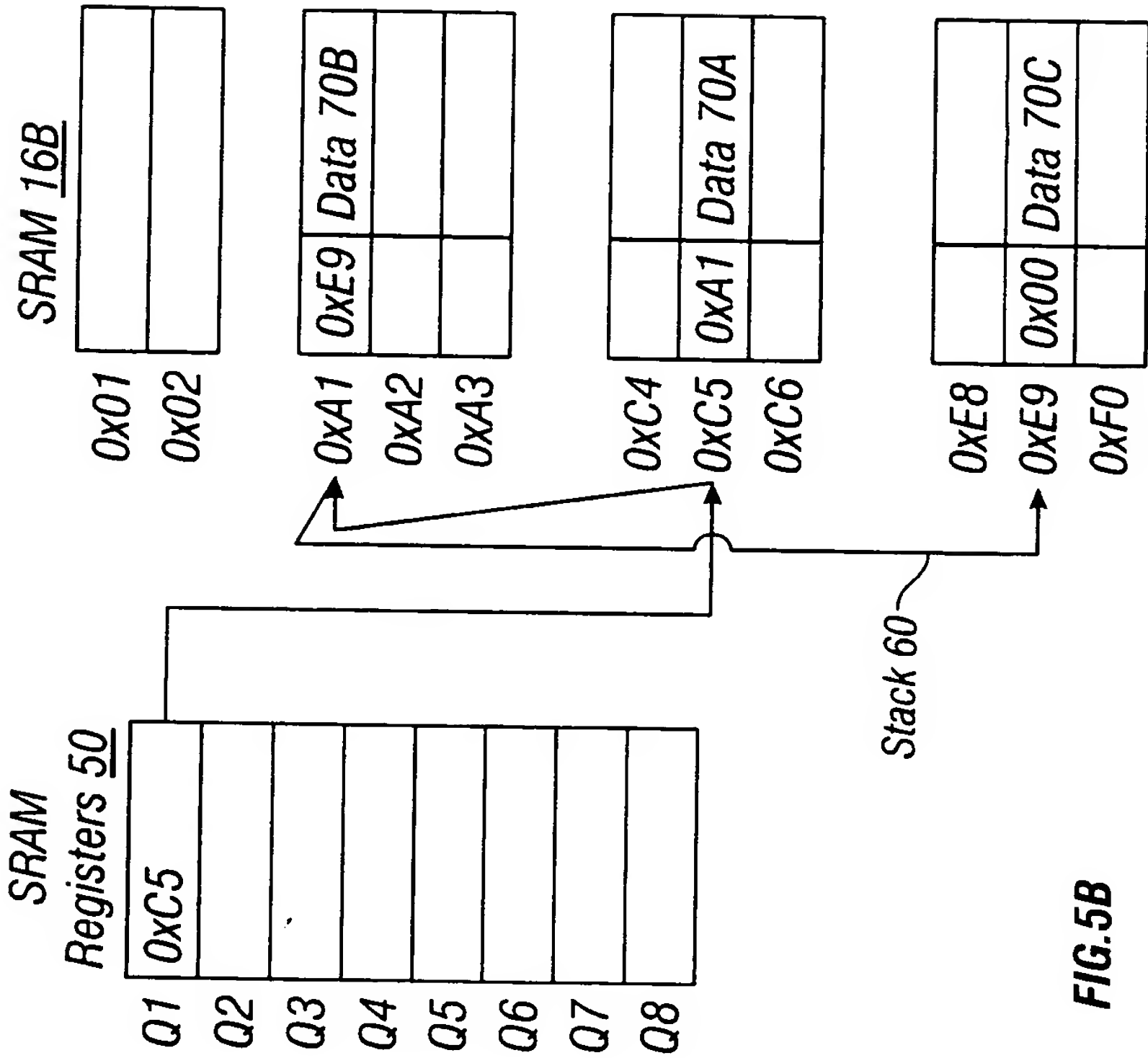


FIG.5B

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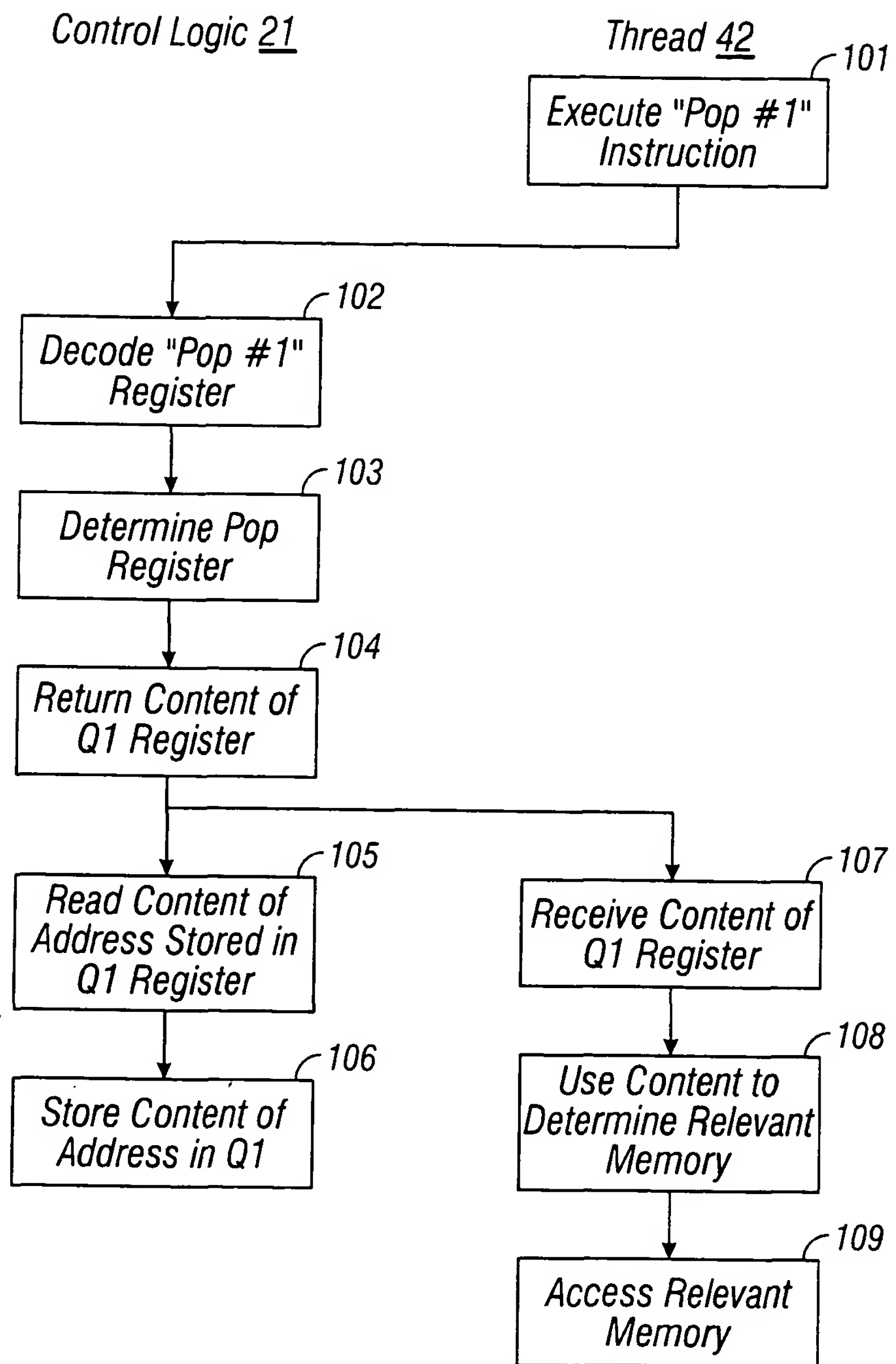


FIG. 6A

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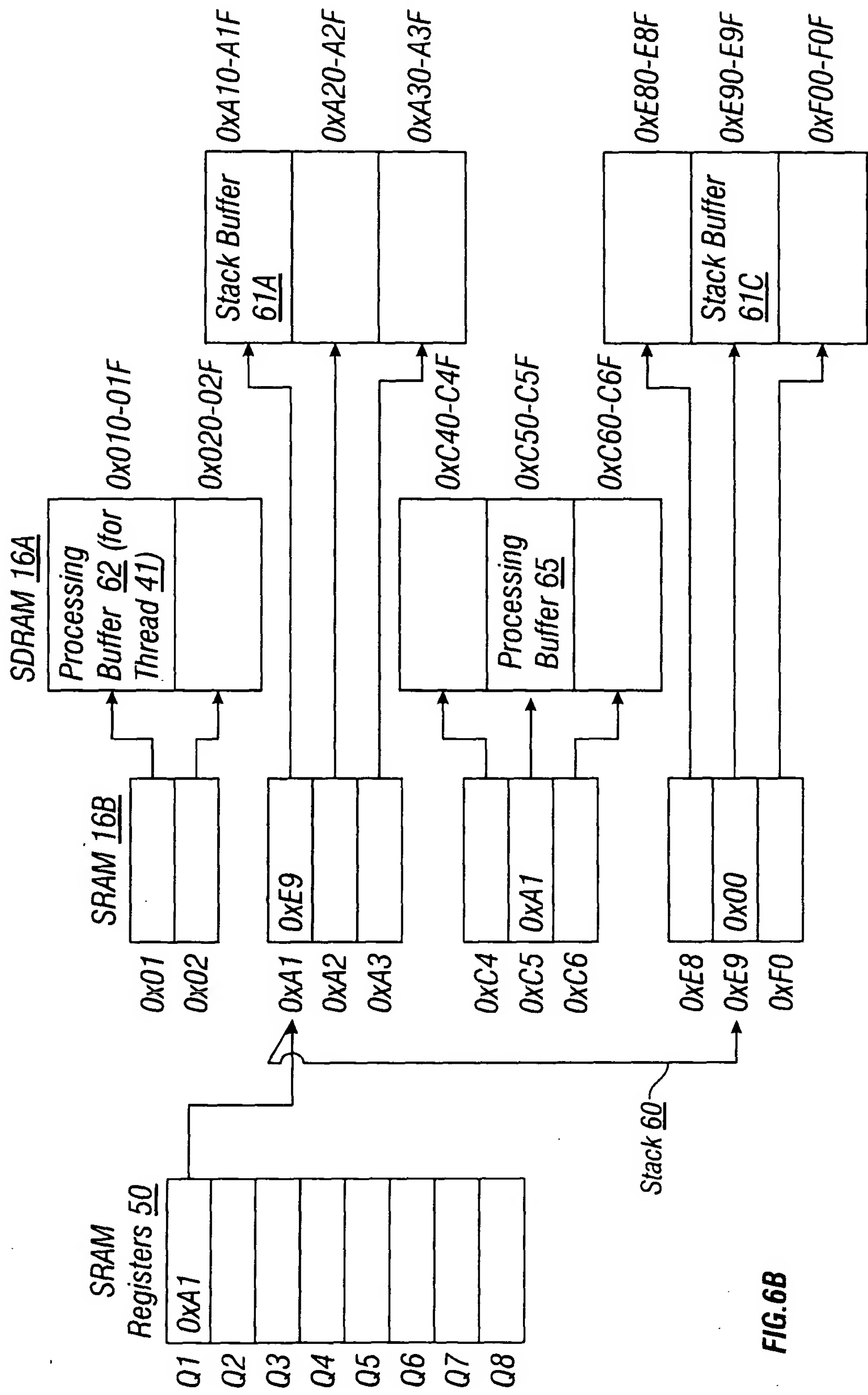
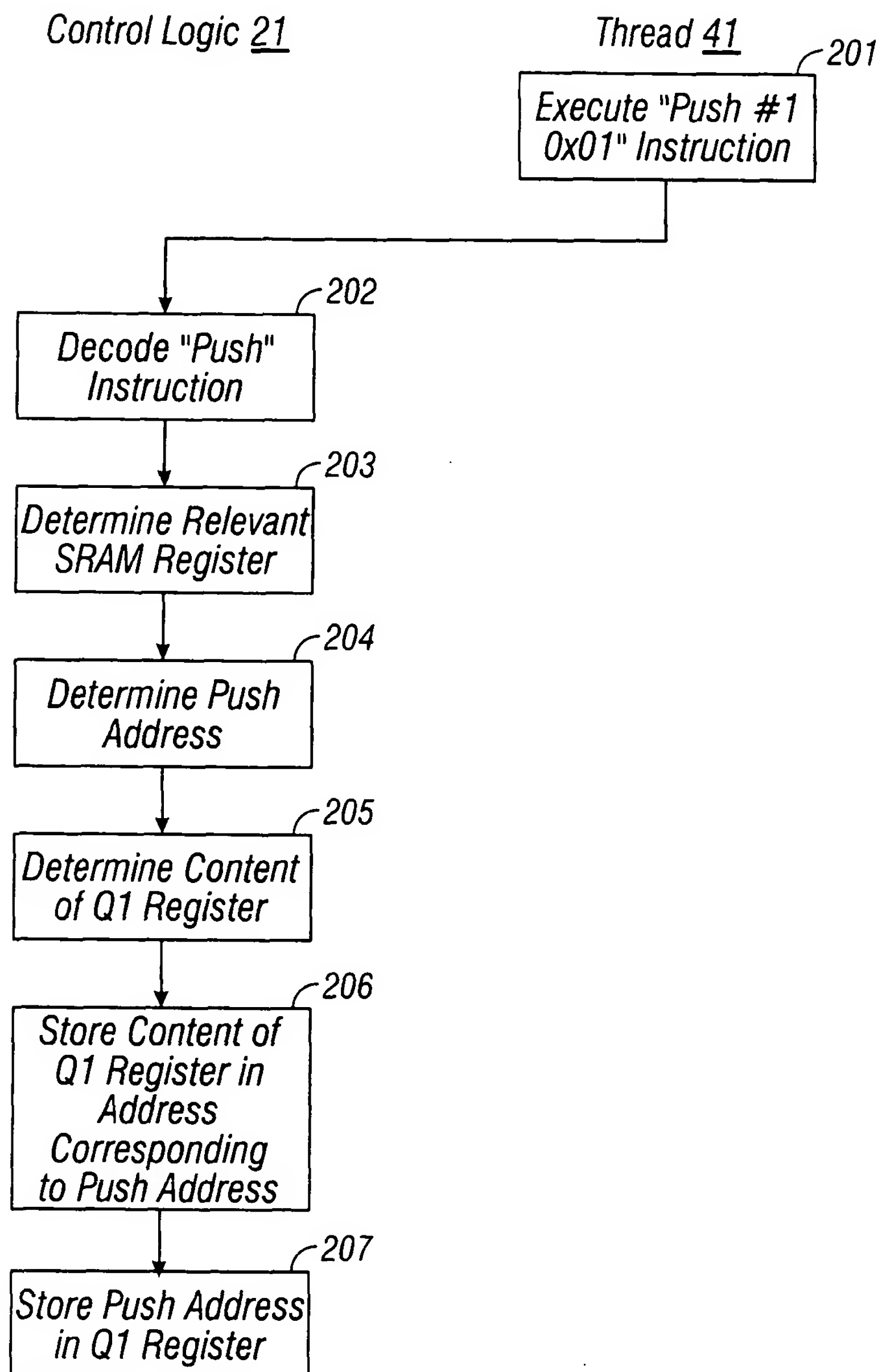


FIG.6B

9/11**FIG. 7A**

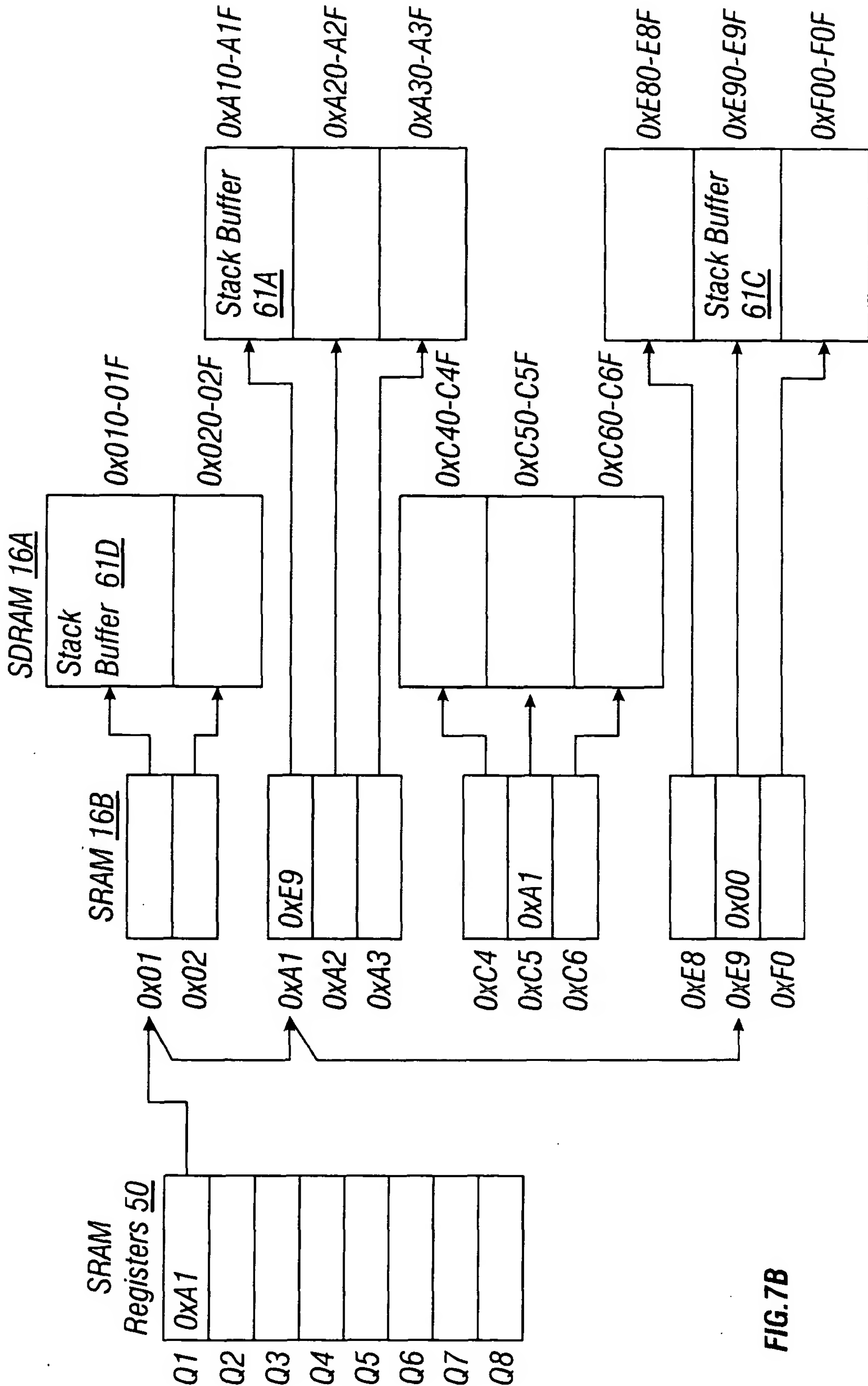


FIG.7B

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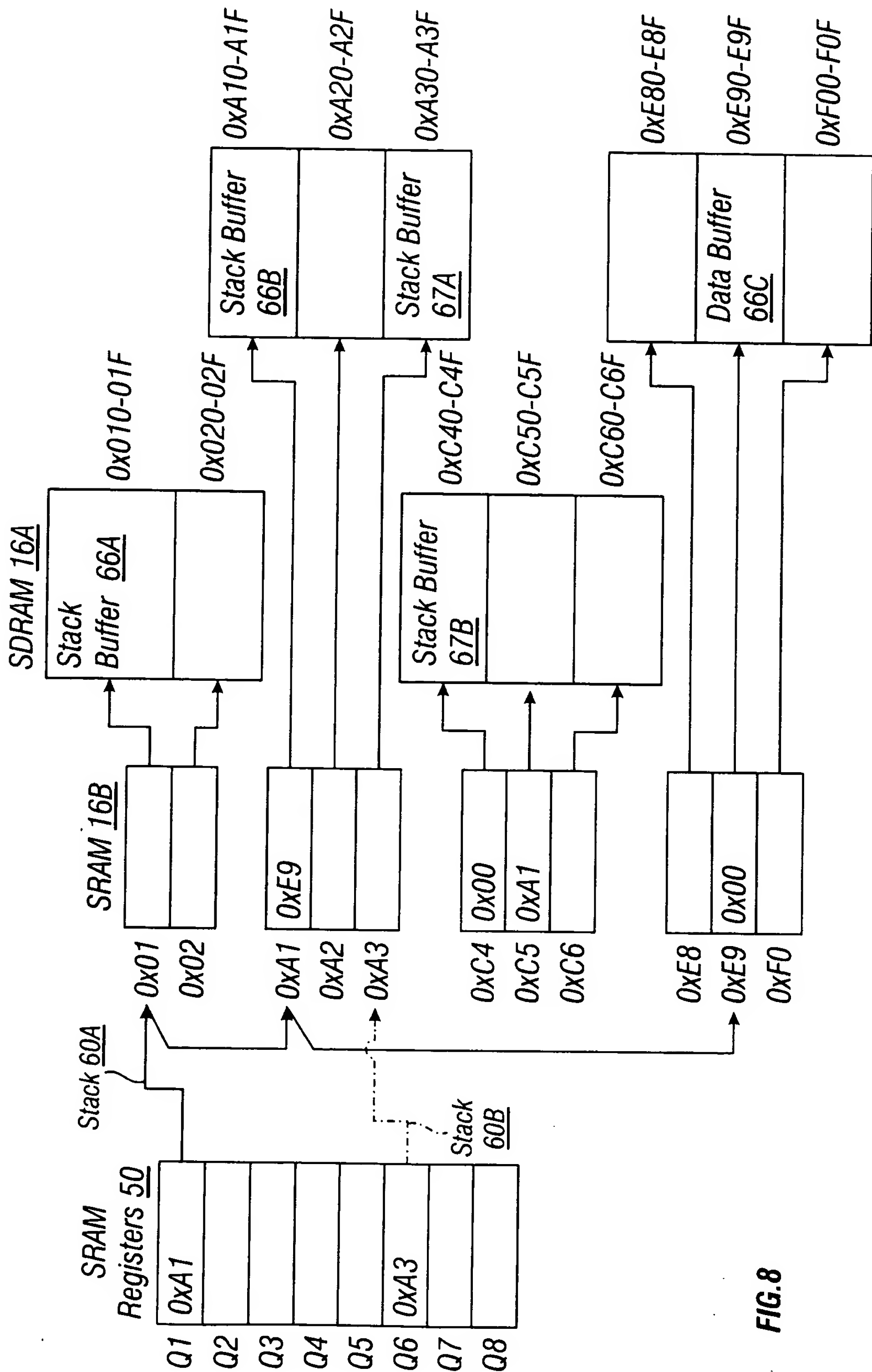


FIG.8